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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10055591	FILING DATE 01/22/2002	CLASS 438	SUBCLASS 152	GAU 2812	EXAMINER H. J. Jore
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\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

REPUBLIC OF KOREA 2001-3519 1/22/2001  
REPUBLIC OF KOREA 2001-78830 12/13/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	PAS203A
Verified and Acknowledged Examiners's initials <i>ML</i>		

TITLE : Semiconductor manufacturing apparatus

U.S. DEPT. OF COMM. / PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Amount Due		Print Fig.	
Date Paid			
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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